

FIG. 1

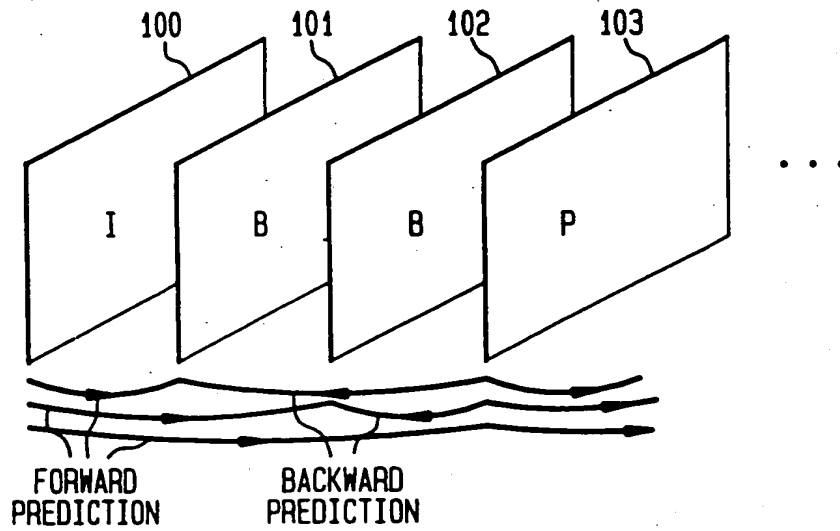
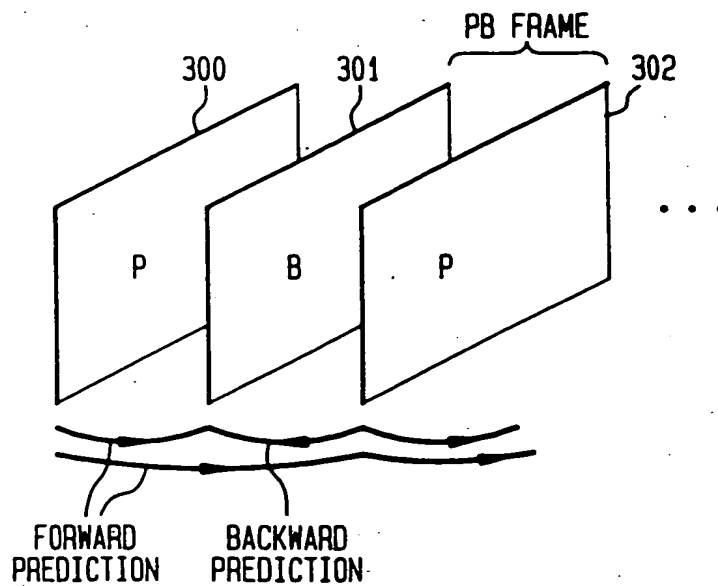
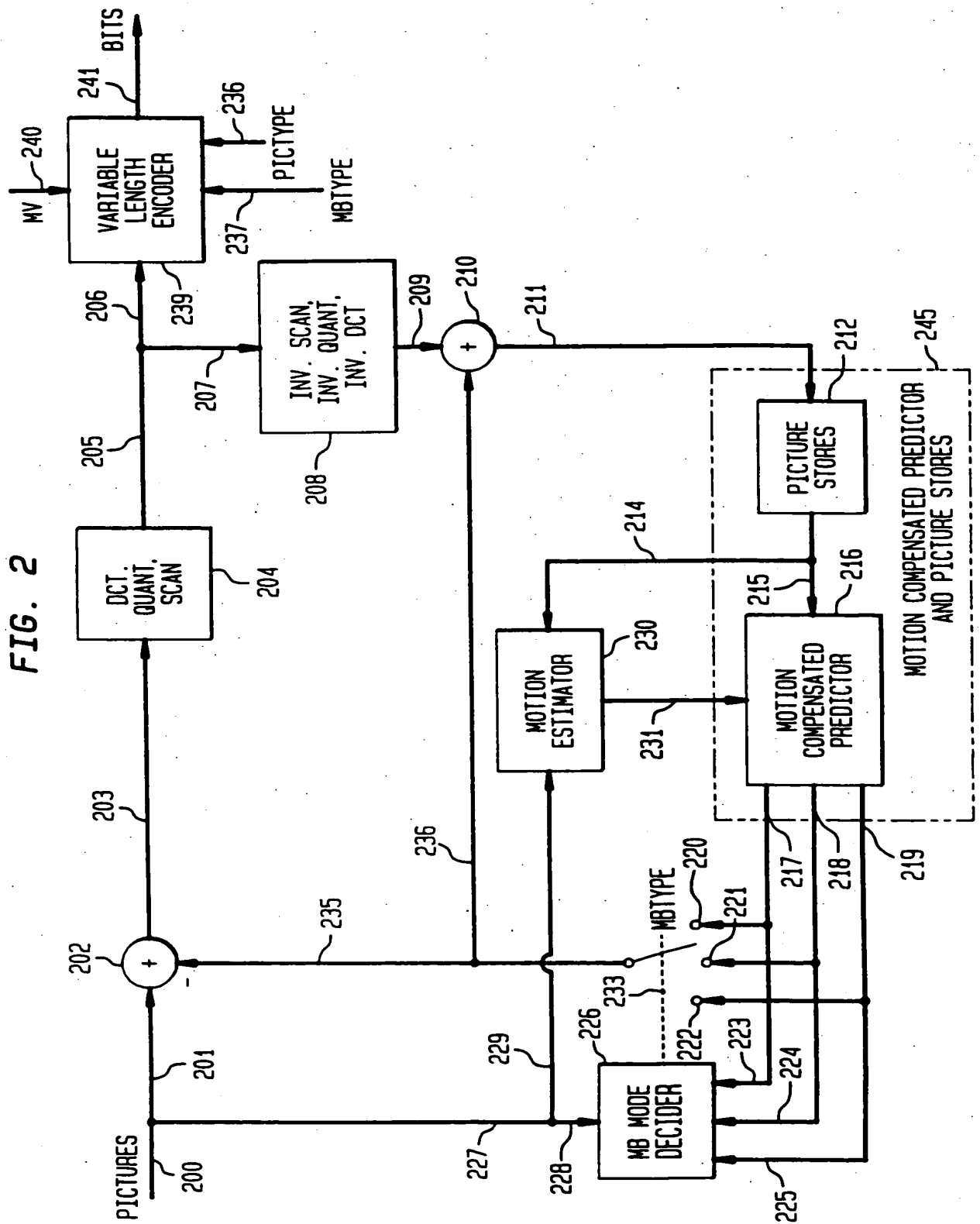
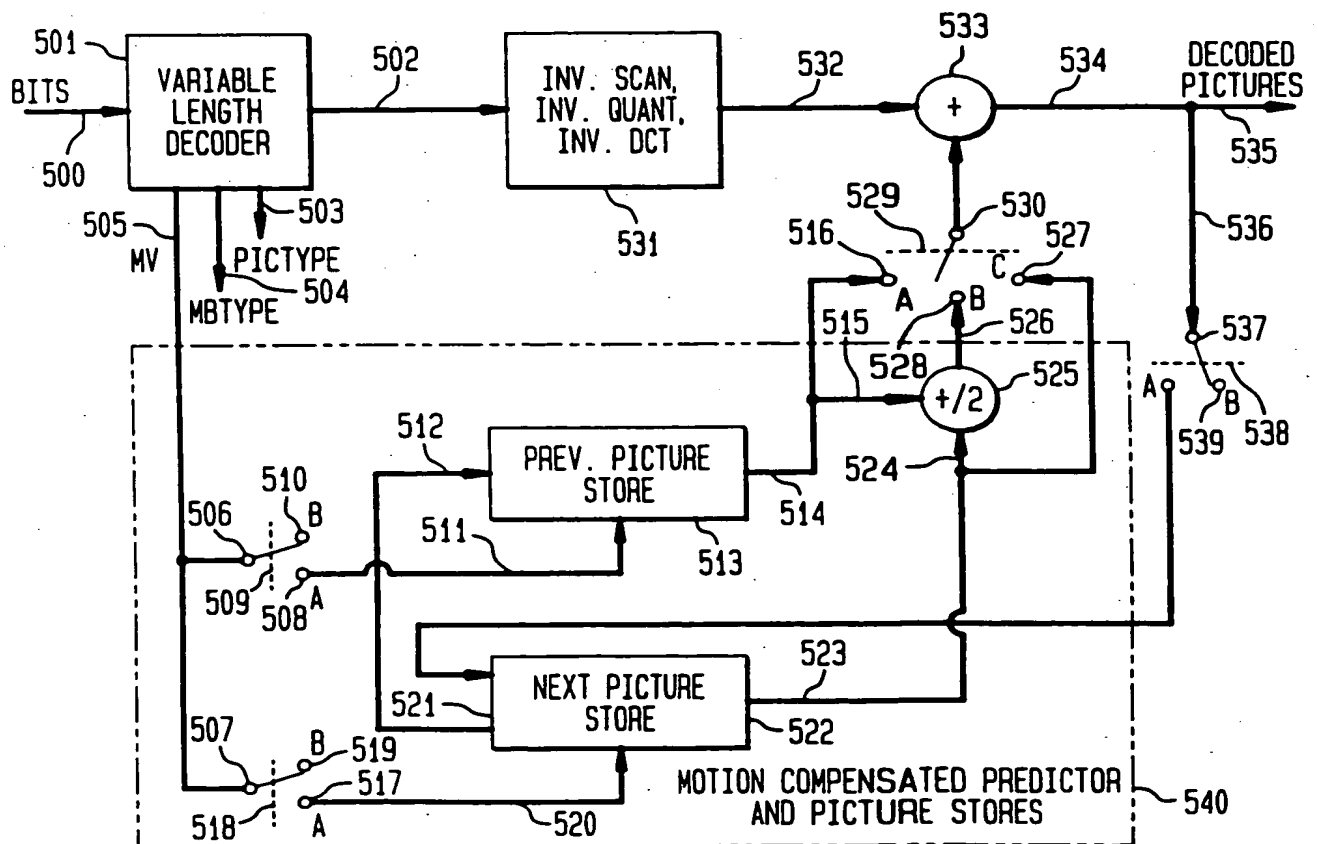


FIG. 3







The diagram illustrates a video decoder system. It begins with an input of **BITS** (600) entering a **VARIABLE LENGTH ENCODER** (601). The output of the encoder (602) goes to an **INV. SCAN, INV. QUANT, INV. DCT** block (626). The output of this block (627) is added to a feedback signal (625) at a summing junction (628). The result (629) is then sent to the **DECODED PICTURES** output (630). A portion of the decoded pictures (631) is fed back through a switch (626) to the summing junction (628). Another portion (632) is fed back through a switch (633) to a **+1/2** block (621). The output of the +1/2 block (620) is added to the output of the INV. DCT block (627) at the summing junction (628). The output of the summing junction (629) is also fed back through a switch (634) to the +1/2 block (621). The output of the +1/2 block (620) is also fed back through a switch (635) to the **PREV. PICTURE STORE** (613) and the **NEXT PICTURE STORE** (617). The output of the PREV. PICTURE STORE (614) is fed back through a switch (615) to the summing junction (628). The output of the NEXT PICTURE STORE (618) is fed back through a switch (619) to the summing junction (628). The output of the summing junction (629) is also fed back through a switch (611) to the **NEXT PICTURE STORE** (617). The output of the summing junction (629) is also fed back through a switch (612) to the **PREV. PICTURE STORE** (613). The output of the summing junction (629) is also fed back through a switch (610) to the **NEXT PICTURE STORE** (617). The output of the summing junction (629) is also fed back through a switch (609) to the **NEXT PICTURE STORE** (617). The output of the summing junction (629) is also fed back through a switch (608) to the **SCALER AND ADDER** (607). The output of the summing junction (629) is also fed back through a switch (606) to the **SCALER AND ADDER** (607). The output of the summing junction (629) is also fed back through a switch (605) to the **SCALER AND ADDER** (607). The output of the summing junction (629) is also fed back through a switch (604) to the **SCALER AND ADDER** (607). The output of the summing junction (629) is also fed back through a switch (603) to the **SCALER AND ADDER** (607). The output of the summing junction (629) is also fed back through a switch (602) to the **SCALER AND ADDER** (607). The output of the summing junction (629) is also fed back through a switch (601) to the **SCALER AND ADDER** (607).

FIG. 7

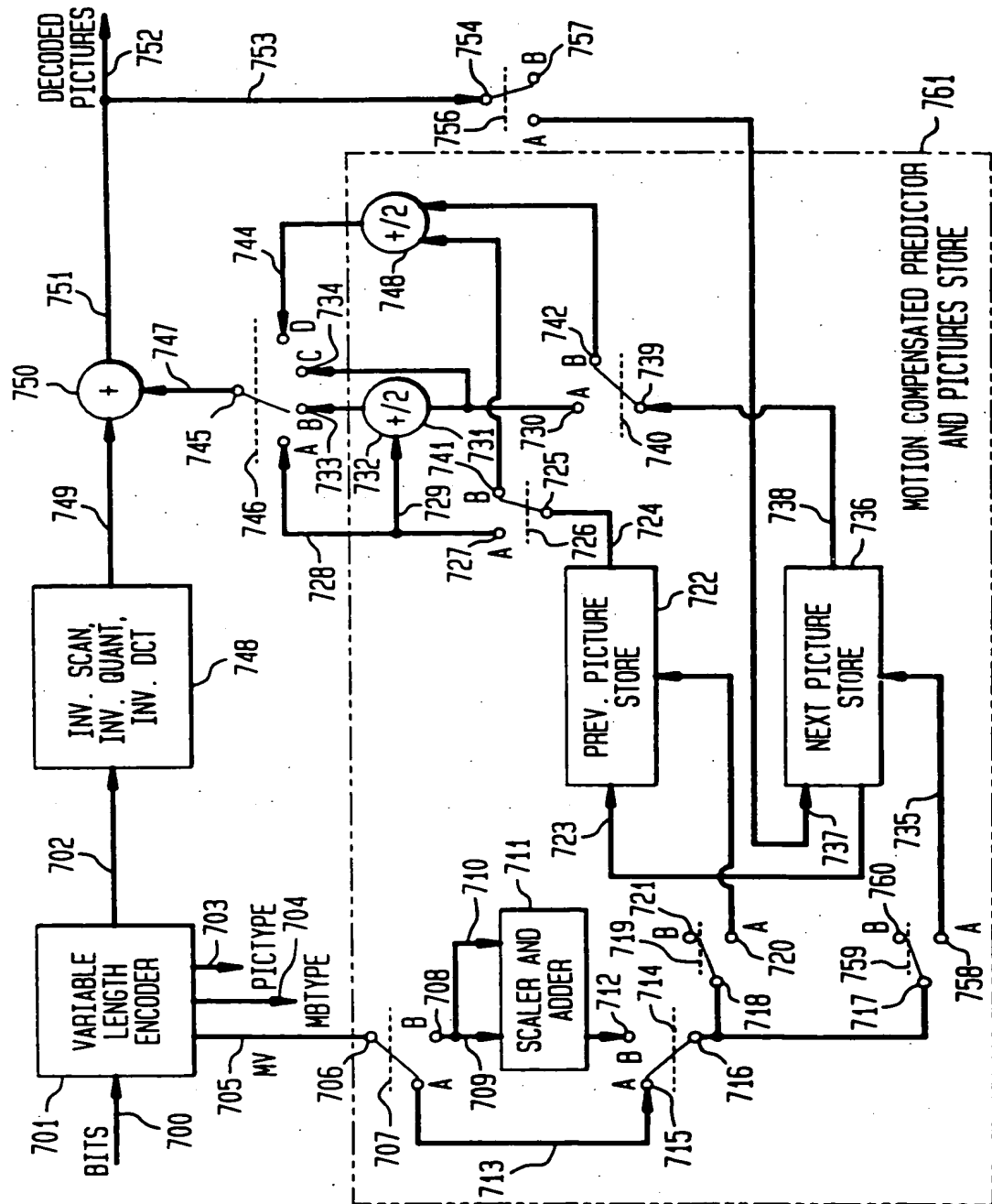


FIG. 8

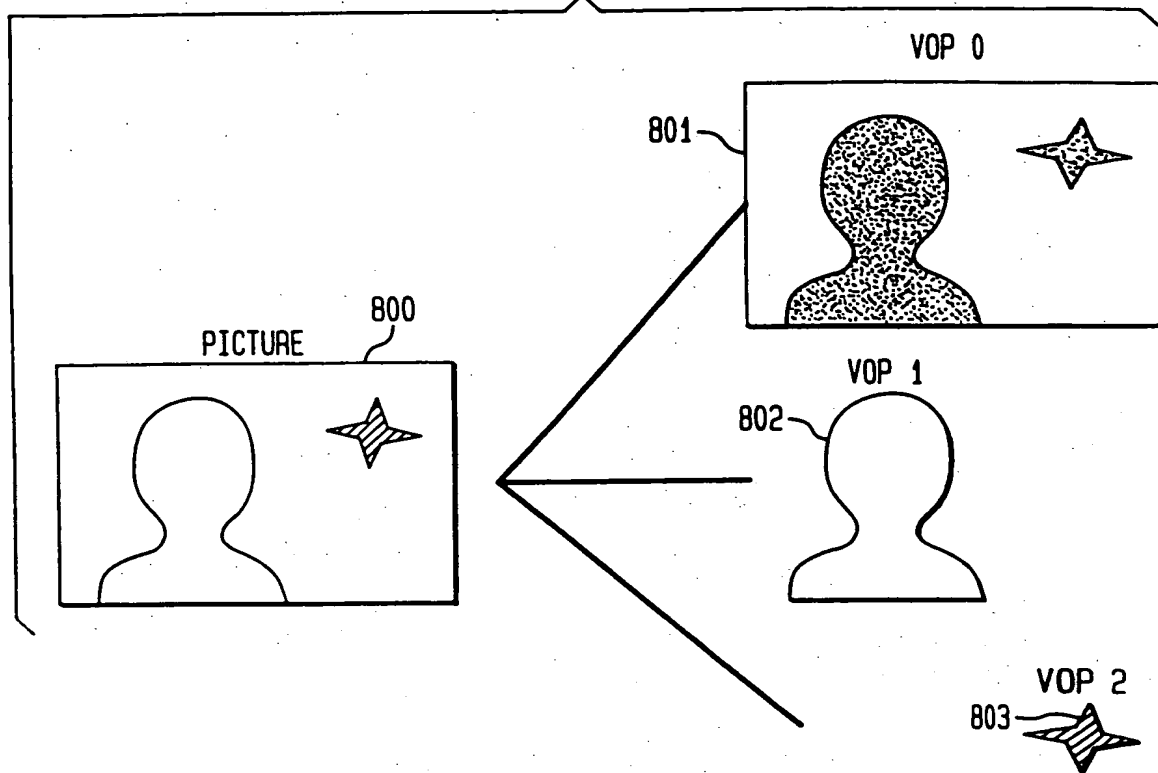


FIG. 9

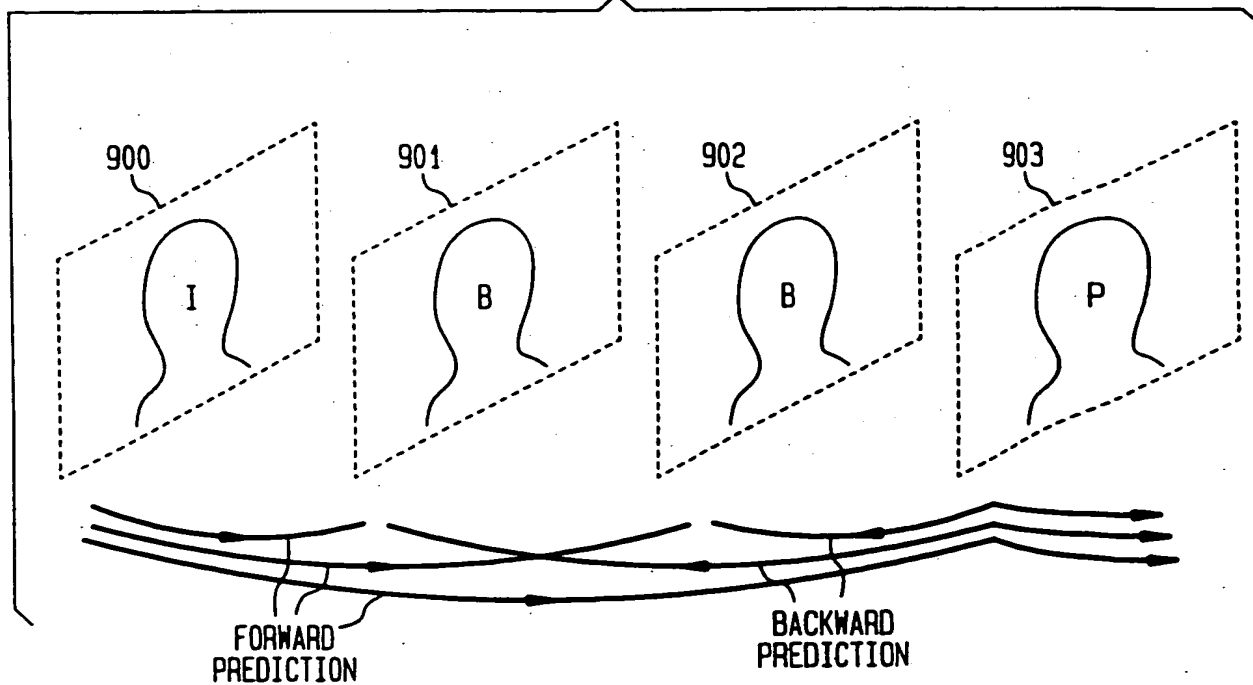


FIG. 10

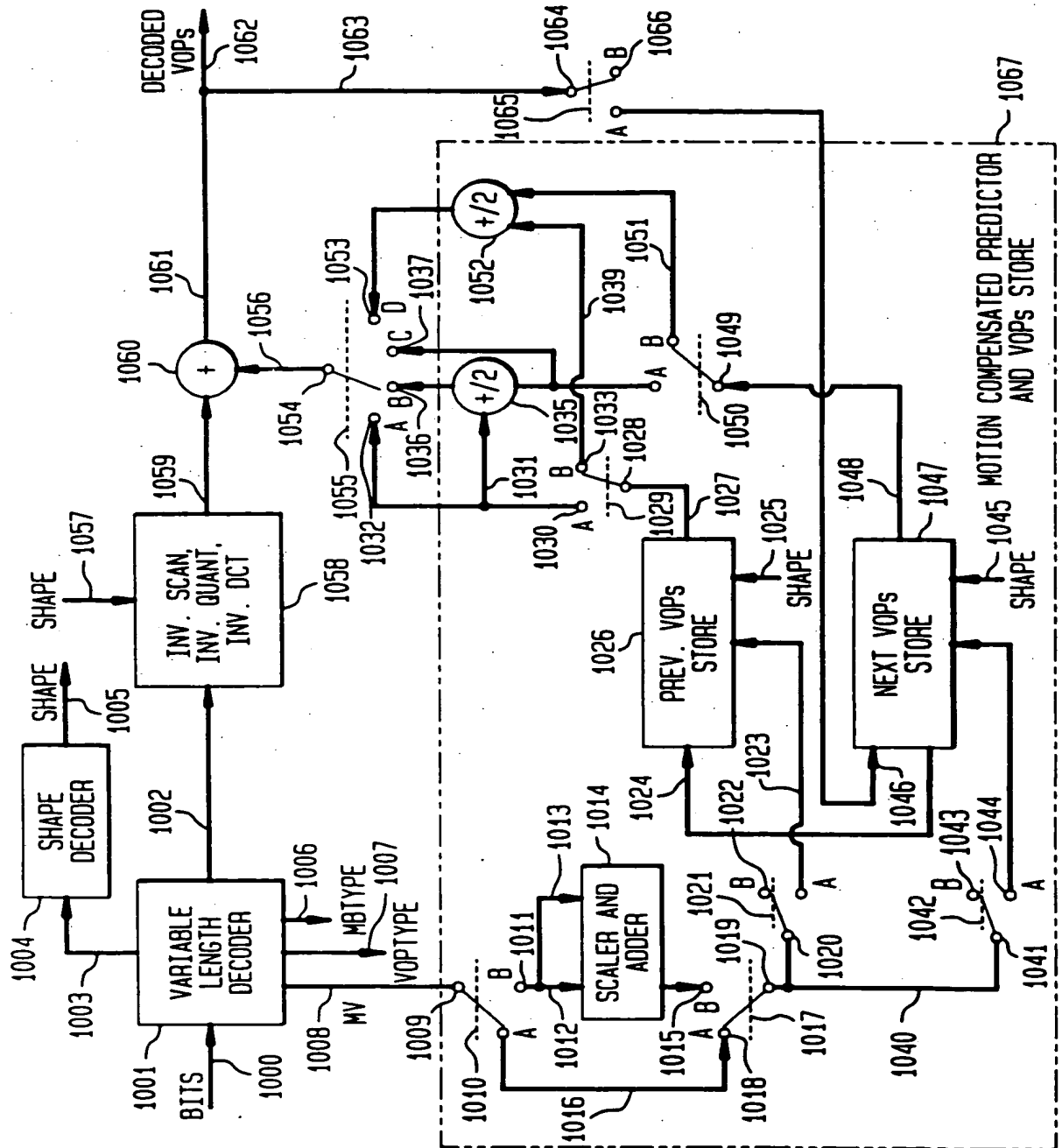


FIG. 11

